Design Change Notice

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Issued by: Research & Development Dept. I / Albert Shao

Product Part Number: GPM8F3133D	**New Product Part Number (if any): GPM8F1006CL**

Modified Item:

☒ Function	☐ Document update	☐ Product phase out
☒ Electrical Characteristics	☐ Application circuit	☐ Ordering limitation
☐ Bonding pad/package	☐ Development Tool, Software, Hardware	

DESCRIPTION:

A. Compare table:

The functions in GPM8F1006CL are mostly identical to that in GPM8F3133D.

Item	GPM8F3133D	GPM8F1006CL
Max. System CLK (MHz)	64	72
Operating Voltage (V)	2.4~5.5	3.2~5.5
Max. I_{STBY}	10uA	37uA
LVR	1.9V, 2.4V, 3.2V, 4.2V	2.9V , 4.2V
LVD	2.1V, 2.6V, 3.4V, 4.4V	3.4V, 4.4V
OP offset	-10mA~+10mV	-16mV~+16mV
FLASH	32KB with 12KB high speed code area	64KB with 12KB high speed code area
XRAM	1.0KB	2.0KB
UART	1	2
Multiplier (signed/unsigned)	16-bit x 16-bit (1set)	16-bit x 16-bit (2sets)
Divider (signed/unsigned)	32-bit / 16-bit (1set)	32-bit / 16-bit (2sets)
Adder (signed/unsigned)	16-bit + 16-bit (1set)	16-bit + 16-bit (2sets)
Subtractor (signed/unsigned)	16-bit – 16-bit (1set)	16-bit – 16-bit (2sets)
Shifter	32-bit (1set)	32-bit (2sets)
Max. IO	35	38
CLK	IOSC16M	IOSC16M XTAL16M
ADC0 channel mapping	CH0-P00 CH1-P01 CH2-P02 CH3-P03 CH4-OP OUT CH5-x CH6-x CH7-x	CH0-P00 CH1-P01 CH2-P02 CH3-P03 CH4-OP OUT CH5-P04 CH6-P45 CH7-VSS

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ADC1 channel mapping	CH0-P04 CH1-P05 CH2-P06 CH3-P07 CH4-P10 CH5-OP OUT CH6-x CH7-x	CH0-P04 CH1-P05 CH2-P06 CH3-P07 CH4-P10 CH5-OP OUT CH6-P45 CH7-VSS
ADC scan mode	--	v
PWM dithering mode	--	v
Timer C Functions	Timer / Counter mode Capture functions(P24, P25, P26, P27)	Timer / Counter mode Capture functions(P24, P25, P26, P27) PWM functions (P24, P25, P26, P27)

Please refer to data sheet for more detailed information.

Action Remark:

- This concludes the DCN -

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