

<b>Customer</b>		<b>Date</b>	
<b>Project Title</b>		<b>Code No.</b>	
<b>Working Voltage</b>	<input type="checkbox"/> 1.8V ~ 3.6V	<b>CPU Frequency (programmable option)</b>	MHz
<b>Release Code File (fill "00H" or "FFH" for unused area)</b>			
<b>Binary Filename:</b>		<b>Binary file check sum:</b>	
		<b>User ROM check sum:</b> (computed by G+IDE tool)	
Stack area located at 0x100 – 0x1FF			<input type="checkbox"/> Check
<b>Code Option</b>			
<b>VDD_ADC</b>	<input type="checkbox"/> External Source	<input type="checkbox"/> Internal Regulator	
<b>Illegal Address Reset</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
<b>Software Reset</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
<b>Watchdog Reset</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
If watchdog is enabled, please check the followings:			
(A). Watchdog should be tested on EMU board.			<input type="checkbox"/> Check
(B). Watchdog port should be cleared within a specified period.			<input type="checkbox"/> Check
<b>Illegal Address Interrupt</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
<b>LCD Probe (CP, LP, FP, FM, Data[7:0])</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
<b>Security</b>	<input type="checkbox"/> Enabled	<input type="checkbox"/> Disabled	
<b>Hardware / Software</b>			
LCD	When system powers on, LCD control port and buffer must be initialized. Otherwise, the data on LCD driver will be randomly generated.		<input type="checkbox"/> Check
	VLCD must be greater than VDD.		
Key scan	When segment outputs are shared with key-scan output port and PortA is used for key-scan input port (pins), all PortA pins for key-scan input must be configured as pull high state.		<input type="checkbox"/> Check
SPI I2C UART	If SPI, I2C, UART interfaces are used: Programmers should set SPI, I2C, UART I/Os to proper status before entering low power mode to avoid extra power consumption.		<input type="checkbox"/> Check
LVD	If LVD is used: Programmers should delay at least 10us to reach a stabilized voltage level after enabling LVD function.		<input type="checkbox"/> Check
PortA or PortB data must be latched before setting wakeup source and before entering sleep mode. That is, the following code must be presented prior to setting wakeup source and entering sleep mode: LDA \$308A ; latch PortA LDA \$3092 ; latch PortB			<input type="checkbox"/> Check

## General Programming Checklist

The general programming checklist intends to provide some general characteristics about GENERALPLUS devices. It is the customer's responsibility to check all the information in the list. No responsibility is assumed by GENERALPLUS for any non-checked box even this confirmation sheet has been approved by GENERALPLUS. Make sure the following conditions are met and verified:

- CPU stack pointer must be reset after system power-on.
- All RAM must be initialized after power on.
- Do not enable interrupt before initializing RAM.
- Sleep port must be cleared and re-initialized before entering sleep mode.
- The used RAM not over the stack reserved area.
- Correct RAM/Flash size and start addresses.
- No current drain on I/O in sleep mode.
- No I/O remains floating in sleep mode.
- Non-used I/O ports should be masked off (for input process).

Example (suppose PortA[3:0] as input and PortA [7:4] are invalid ):

LDA PortA ; read I/O port A Data

AND #0FH ; mask off high nibble

☐ Check-by ticking off this box, customer understands that the above conditions are fully met and verified.

## Document Version

Programming guide title and version: \_\_\_\_\_

User's guide title and version: \_\_\_\_\_

Application Note No (if any): \_\_\_\_\_

Other documents (if any): \_\_\_\_\_

## Development Tool / Board Version

EV/Piggyback board version: \_\_\_\_\_

Development Tool & version: \_\_\_\_\_

Software / Hardware tools version: \_\_\_\_\_

### Customer Note

### Generalplus Note

Signature: \_\_\_\_\_

Signature: \_\_\_\_\_

Note: Please send/fax this form to GENERALPLUS. We will countersign and return it back after approval.